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Intellectual Property Administration  
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 **ORIGINAL**

PATENT APPLICATION  
ATTORNEY DOCKET NO. 200302253-2

IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Luiz A. BARROSO et al.

Confirmation No.: 2813

Application No.: 10/698,130

Examiner: T. V. Nguyen

Filing Date: 10/31/2003

Group Art Unit: 2187

Title: MULTIPROCESSOR CACHE COHERENCE SYSTEM AND METHOD IN WHICH  
PROCESSOR NODES AND INPUT/OUTPUT NODES ARE EQUAL PARTICIPANTS

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

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TERMINAL DISCLAIMER  
RESPONSIVE TO A DOUBLE PATENTING REJECTION

Sir:

Petitioner, Hewlett-Packard Development Company, L.P. is the owner of 100 percent interest in the instant application. Petitioner hereby disclaims, except as provided below, the terminal part of the statutory term of any patent granted on the instant application, which would extend beyond the expiration date of the full statutory term defined in 35 U.S.C. 154 to 156 and 173 as shortened by any terminal disclaimer of prior Patent No. 6,675,285 to Hewlett-Packard Development Company, L.P. which issued on 1/08/2004 and is commonly owned by Petitioner. Petitioner hereby agrees that any patent so granted on the instant application shall be enforceable only for and during such period that it and the prior patent are commonly owned. This agreement runs with any patent granted on the instant application and is binding upon the grantee, its successors or assigns.

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